

Atty Docket No.: JCLA12566

Serial No.: 10/801,629

AMENDMENTSIn The Claims:

Please amend the claims according to the following listing of claims and substitute it for all prior versions and listings of claims in the application.

Claim 1 (currently amended) A method for generating a wobble clock signal, comprising:

generating a wobble clock signal according to a wobble signal, wherein the wobble signal is generated when an optical disc is processed;

comparing a width of the wobble signal at different status with an average, wherein the average is obtained by averaging a plurality of counting data generated by counting the width of the wobble signal at different status; and

generating the wobble clock signal according to ~~either~~ one of the following:

continuously generating the wobble clock signal ~~according to the wobble signal~~; and

generating the wobble clock signal by feeding ~~feedbacking~~ the wobble signal back to itself.

Claim 2 (currently amended) The method for generating the wobble clock signal of claim 1, wherein the step of generating the wobble clock signal according to the result of comparing the width of the wobble signal at different status with the average, further comprises:

~~counting the width of the wobble signal at different status, so as to generate a plurality of counting data;~~

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generating a plurality of comparison data according to a relationship between the counting data and the average; and

determining whether or not to select the wobble clock signal and feedback feed the wobble clock signal back to generate the wobble clock signal, according to whether the comparison data is beyond a predetermined range or not.

Claim 3 (currently amended) The method for generating the wobble clock signal of claim 2, further comprising:

enabling a deformation signal, when the comparison data is beyond the predetermined range; and

feedbacking feeding the wobble clock signal back to generate a new wobble clock signal by replacing the original wobble clock signal, when a number of continuously enabling the deformation signal is beyond a predetermined value.

Claim 4 (canceled)

Claim 5 (original) The method for generating the wobble clock signal of claim 1, wherein the wobble signal is processed by a fake signal removing process in advance.

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Claim 6 (currently amended) A method for generating a wobble clock signal, comprising:
counting a width of a wobble signal at different status which is generated when an optical disc is processed and generating a plurality of counting data; ~~and~~
generating a wobble clock signal according to an average of the counting data; and
feeding the wobble clock signal back to re-generate the wobble clock signal by replacing the original wobble clock signal, when either a defect is found on an optical disc or the wobble signal becomes deformed.

Claim 7 (original) The method for generating the wobble clock signal of claim 6, further comprising:
generating an average clock signal according to the average; and
generating the wobble clock signal by dividing a frequency of the average clock signal by N, wherein N is a positive integer.

Claim 8 (original) The method for generating the wobble clock signal of claim 7, wherein the average is an average of half cycle of the wobble signal.

Claim 9 (canceled)

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Claim 10 (currently amended) The method for generating the wobble clock signal of claim [[9]] 6, wherein whether or not the wobble signal becomes deformed is determined by the steps comprising:

counting the width of the wobble signal at different status, so as to generate a plurality of counting data;

generating a plurality of comparison data according to a relationship between each of the counting data and [[an]]the average; and

determining whether or not to select the wobble clock signal and ~~feedback~~ feed the wobble clock signal back to generate the wobble clock signal according to whether the comparison data is beyond a predetermined range or not.

Claim 11 (currently amended) The method for generating the wobble clock signal of claim 10, further comprising:

enabling a deformation signal when the comparison data is beyond the predetermined range; and

selecting the wobble clock signal and ~~feedbacking~~ feeding the wobble clock signal back to generate the wobble clock signal when a number of continuously enabling the deformation signal is beyond a predetermined value.

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Claim 12 (original) The method for generating the wobble clock signal of claim 10, wherein the average is the average of half cycle of the wobble signal.

Claim 13 (original) The method for generating the wobble clock signal of claim 6, wherein the wobble signal is processed by a fake signal removing process in advance.

Claim 14 (currently amended) An apparatus for generating a wobble clock signal, comprising:

a clock signal generating circuit, for selecting one of a wobble signal, which is generated when an optical disc is processed, and a signal, which is fed back from the wobble clock signal, so as to generate the wobble clock signal; and

a selection circuit, electrically coupled to the clock signal generating circuit for receiving the wobble signal and the signal fed back from the wobble clock signal, and for determining whether or not to allow the clock generating circuit to select the wobble clock signal and ~~feedback~~ feed the wobble clock signal back to generate the wobble clock signal itself according to at least an enabling signal.

Claim 15 (currently amended) The apparatus for generating the wobble clock signal of claim 14, further comprising a deformation detecting module for receiving the wobble signal,

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and for determining whether or not to output a deformation enabling signal according to a result of comparing a width of the wobble signal [[on]] at different status with an average, wherein the average is obtained by averaging a plurality of counting data generated by counting the width of the wobble signal at different status.

Claim 16 (currently amended) The apparatus for generating the wobble clock signal of claim 15, wherein the deformation detecting module comprises:

a first counter for counting the width of the wobble signal at different status so as to generate ~~a plurality of the~~ counting data; and

a comparator electrically coupled to the first counter for comparing the counting data with the average so as to obtain a plurality of comparison data, wherein when the comparison data is beyond a predetermined range, the comparator generates a deformation signal, and the deformation detecting module determines whether or not to output the deformation enabling signal according to the deformation signal.

Claim 17 (currently amended) The apparatus for generating the wobble clock signal of claim 16, wherein the deformation detecting module further comprises a deformation signal counting circuit electrically coupled to the comparator for counting a number of continuously outputting the deformation signal, when the number of continuously outputting the deformation

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signal is greater than ~~[[the]]~~ an average of the counting data, the deformation detecting module generates the deformation enabling signal.

Claim 18 (currently amended) The apparatus for generating the wobble clock signal of claim 14, wherein the ~~[[feedback]]~~ clock signal generating circuit comprises:

a second counter electrically coupled to the selection circuit, wherein the selection circuit receives the wobble signal and the wobble clock signal, selects and sends one of the signals to the second counter, and the second counter is used to count a width at different status of the signal input by the selection signal so as to generate a plurality of counting data;

a low-pass filter electrically coupled to the second counter for generating an average clock signal by averaging the counting data; and

a clock signal generating circuit electrically coupled to the low-pass filter for generating the wobble clock signal by dividing a frequency of the average clock signal by N, wherein N is a positive integer.

Claim 19 (currently amended) The apparatus for generating the wobble clock signal of claim 18, wherein the clock generating circuit further comprises a pregroove absolute-time decoding circuit electrically coupled to the selection circuit, the second counter, and the low-pass ~~[[filer]]~~ filter respectively for generating a pregroove absolute time data according to the

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counting data, the average clock signal, and one of the wobble signal and the wobble clock signal.

Claim 20 (original) The apparatus for generating the wobble clock signal of claim 14, wherein the selection circuit comprises:

a multiplexer for receiving the wobble signal and the signal fed back from the wobble clock signal, wherein the multiplexer is electrically coupled to the clock signal generating circuit for selecting and outputting one of the wobble signal and the signal fed back from the wobble clock signal to the clock signal generating circuit; and

an OR gate for receiving the deformation enabling signal and a defect enabling signal which is generated when a defect is found on the recordable optical disc, and when at least one of the deformation enabling signal and the defect enabling signal is enabled, causing the multiplexer to select and send the signal fed back from the wobble clock signal to the clock signal generating circuit.